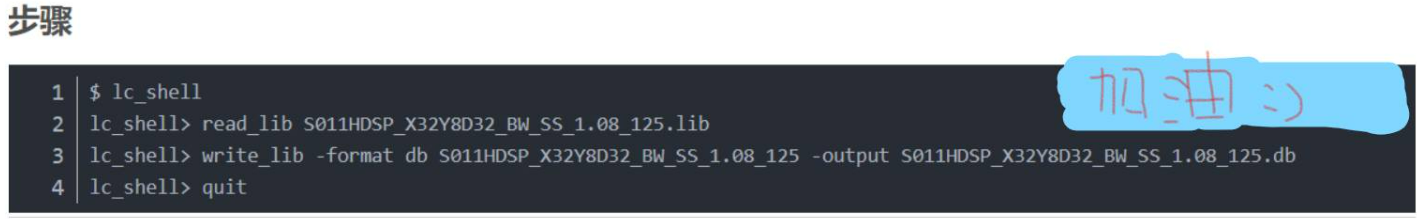
**Convert the .lib to .db for synthesis**



-----------------------------------------------------------------------------------------------------------------

**If you are not familiar with verilog, we recommend a website for learning verilog**

* https://hdlbits.01xz.net/wiki/Main\_Page

--> Answer: https://blog.csdn.net/wangkai\_2019/article/details/106664283